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APPLICANTS

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**** CONTINUING DATA *******

This application is a 371 of PCT/JP04/08594 06/11/2004

**** FOREIGN APPLICATIONS *******

JAPAN 2003-316774 09/09/2003
 JAPAN 2003-407206 12/05/2003

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ****

| Foreign Priority claimed | <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No | <input type="checkbox"/> Met after Allowance | STATE OR COUNTRY | SHEETS DRAWINGS | TOTAL CLAIMS | INDEPENDENT CLAIMS |
|--------------------------------|---|--|------------------|-----------------|--------------|--------------------|
| 35 USC 119(a-d) conditions met | <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No | | | | | |
| Verified and Acknowledged | /CHRISTOPHER RAY LAMB/ Examiner's Signature | Initials | JAPAN | 27 | 36 | 1 |

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TITLE

Phase error detecting circuit and synchronization clock extraction circuit

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